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PATENT
8008-1052

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Yasutaka NAKASHIBA

Conf. 2273

Application No. 10/812,282

Group 2815

Filed March 30, 2004

Examiner Jesse A. Fenty

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

AMENDMENT AFTER FINAL REJECTION

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 22, 2007

Sir:

Supplemental to the response filed on March 26, 2007,
please further amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing
of claims that begins on page 2 of this paper.

Remarks begin on page 8 of this paper.